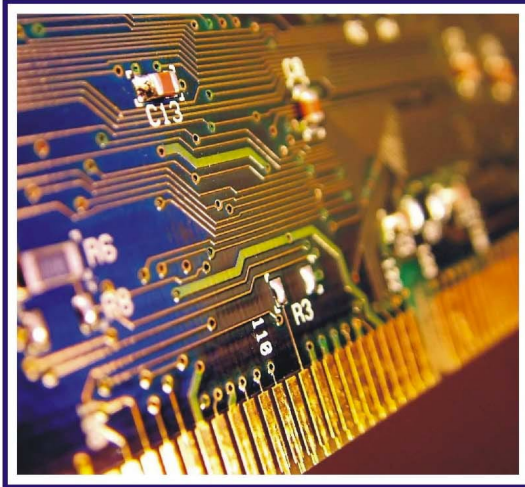


# ELECTRONICS

## ANALOG AND DIGITAL



R Singh • I Singh • B Singh

## Electronics Analog and Digital

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## Product Description

Written with the beginner in mind, this book provides an exceptionally clear and precise detail of electronics. Its approach is explanatory and language is lucid and communicable. Analog and digital electronics described with the help of its circuit designs, pictorial representation and implementation to have clear ideas. Modern aspects of electronics have been introduced from the first principles and discussed in an easy manner so that a beginner can grasp the analog and digital electronics. Each chapter contains solved example problems and exercise problems.



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## Author

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**R. Singh** "R Singh, MCA is equipped with an extraordinary calibre and appreciable academic potency. He has teaching experience of nearly twenty years. He has authored ten books on various complex topics of computer science. He has already submitted his Ph.D. thesis in the field of system simulation. His other areas of interest include Software Engineering, Data Structures and Information Systems.

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