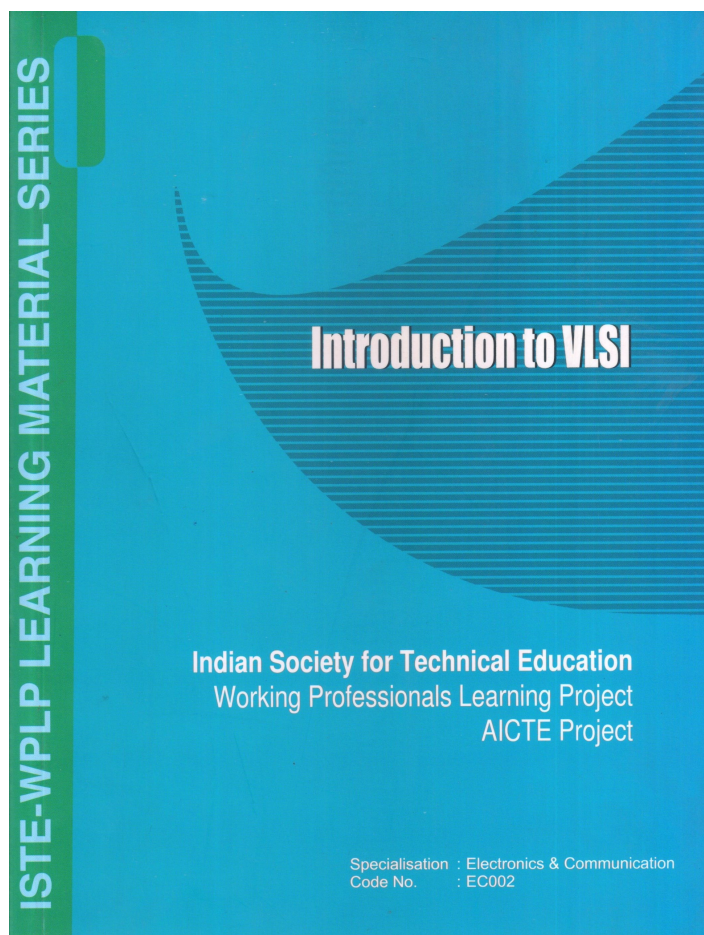




Introduction to VLSI

Author :	M. Chandeasekhar
ISBN 13 :	978-93-55382-38-2
ISBN 10 :	93-55382-38-3
E-ISBN 13 :	978-93-55382-38-2
Edition :	1
Pages :	376
Type of book :	Paperback
Weight (g) :	770.00
Year :	2014
Language :	English
Publisher :	Khanna Publishing House
M.R.P :	Rs 525.00
Categories :	Electrical, Electronics & Communication Engineering, ISTE Series
SKU :	1725537401
Condition Type :	New
Country Origin :	India

Product Description

The growing applications of Very Large Scale Integration in all the fields of electronics, particularly consumer and professional electronics, demand the working engineers/professionals to know more about VLSI. With this objective in view, the Indian Society for Technical Education (ISTE), which is a non-profit organization, requested the authors to prepare Learning Materials on VLSI for the working professionals. The material in this Book is prepared by emphasizing the basics of CMOS (Complementary Metal Oxide Semiconductors) Transistors, their characterization, process, Logic circuits, subsystems, and the design of systems using Computer Aided Design (CAD). This material gives the overview of VLSI, particularly the digital circuits and systems. The topics for this book are based on many text books on VLSI and from materials given in various websites. We thank all the authors and publishers of the books and websites.



Khanna Publishing House

4C/4344, Ansari Road, Daryaganj, New Delhi-110002

Email: contact@khannabooks.com | Tel: 011-2324 44 47 - 48 | Mobile: + +91-99109 09320

Table of Contents

FOREWORD

PREFACE

Chapter 1: Introduction to VLSI.

Chapter 2: CMOS Processing.

Chapter 3: Characteristics of CMOS Components.

Chapter 4: Logic Gates.

Chapter 5: Sequential Logic/ Machines.

Chapter 6: Subsystems.

Chapter 7: VLSI Design Methodologies.

Chapter 8: HDL.

Chapter 9: VLSI CAD Systems.

Abbreviations and Symbols

References



Khanna Publishing House

4C/4344, Ansari Road, Daryaganj, New Delhi-110002

Email: contact@khannabooks.com | Tel: 011-2324 44 47 - 48 | Mobile: + +91-99109 09320

Authors

V. Venkateswarlu Principle, VTU Extension Centre, UTL Technologies Ltd, Bangalore. After his B.Sc degree from Andhra University, V. Venkateswarlu obtained his B.E. in Electrical Communication Engineering and M.E. in Electronics from Indian Institute of Science in 1966 and 1968. He worked on a CISR project "Micropower Electronics" during 1968-1971 under the guidance of Prof. B. S. Sonde. He joined the semiconductor and IC division of Bharat Electronics in 1971 and was working up to 1985. He designed, developed and pilot productionized discrete silicon semiconductor devices, TTL IC'S Linear Bipolar IC'S for Radio and Television; and working on the first, CAD system for layout design of IC'S during the period 1971-1985. During 1983-1984 he was teaching " IC Design USING cad" at Indian Institute of Science. Dr. V. Venkateswarlu is a Fellow of Institute of Electronics and Telecommunication Engineering (INDIA). He is a member of IEEE (USA) and VLSI Society of India. He was on various Committees set up by Govt. of India on Microelectronics and Telecommunications. **M. Chandrasekhar** Vice President, United Telecoms Ltd, Bangalore. Chandrasekhar has over 25 years of extensive experience in the areas of VLSI Education, Establishment of VLSI Design Centers, VLSI Design & Testing, Embedded Software, Project Management, e-Governance projects,.. He has held various technical positions in Ministry of Information Technology, Govt. of India. He conceived a major National Program for development of Special Manpower in the area of VLSI Design & Related Software at UG and PG levels in 19 premier institutions. He has designed & development Real-Time Application Software for Strategic Projects and also development several ASICs for industry as import substitution. He was instrumental in establishment of Centre of VLSI Design & Prototyping (CVDP) under UNDP Program, Ten Industrial VLSI Design Centres at SCL/ITI/C-DAC/ER&DC and Eight educational VLSI Design Centres. He is currently working for United Telecom Group as Vice President in the areas of Telecom and e-Governance projects. He was Program Coordinator for M. Tech (VLSI Design & Embedded Systems) program under Viveswaraya Technological University. He received B.E. in 1974 from Sri Venkateswara University, Tirupati, MSc (Tech) in 1976 from Birla Institute of Technology and Science, Pilani and M.S. in 1985 from McGill University, Canada. He was a Member to VTU Board of Studies in Electronics & Communication Engineering 2004-07.

