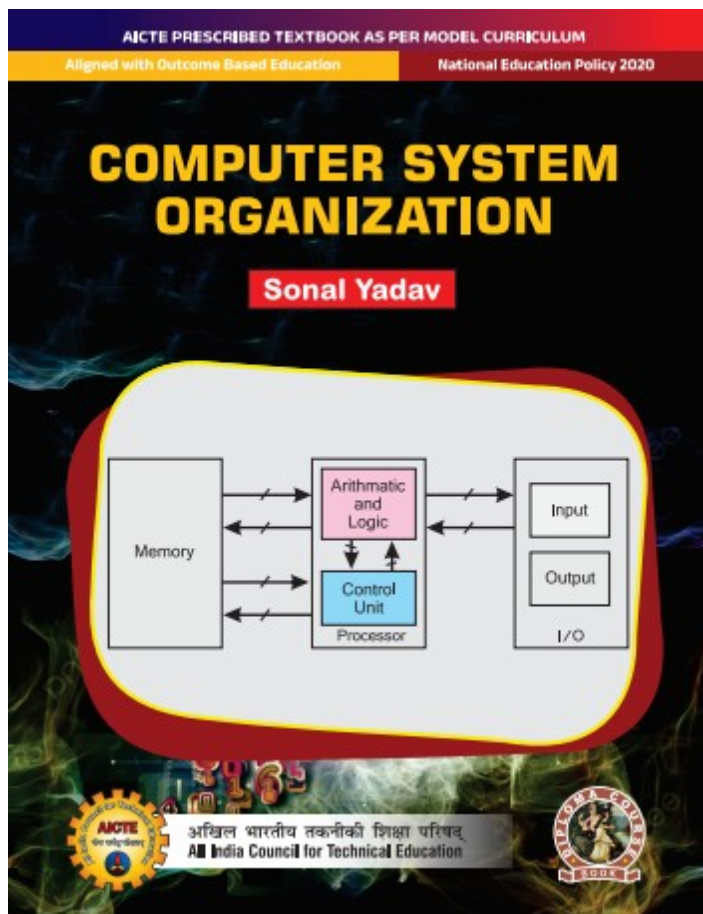




Computer System Organization

Author :	Sonal Yadav
ISBN 13 :	978-93-55387-71-4
ISBN 10 :	93-55387-71-7
E-ISBN 13 :	978-93-55387-71-4
Edition :	First
Pages :	184
Type of book :	Paperback
Year :	2026
Language :	English
Publisher :	Khanna Publishing House
M.R.P :	Rs 348.00
Categories :	AICTE Prescribed Textbooks, English Books
Condition Type :	New
Country Origin :	India

Product Description

Computer System Organization The key concepts of computer system organization are covered in this book. All the fundamental concepts of computer structures, control unit design arithmetic operations, microprocessor architecture. Assembly language programming, and memory interfacing with input-output devices are covered in the book. This book combines theoretical knowledge with practical applications. It also presents state-of-the-art experiments using Verilog HDL language, 8086 microprocessor, NASM assembler, and Gem5 simulator. Each topic can be studied further to advance levels by scanning QR codes provided in the chapters. This book is written for diploma and undergraduate students of CSE, IT, ECE, and MCA to strengthen the ability to analyze and solve simple to complex computer system organization problems. In 'know more' section, notable Indian inventors as well as rich Indian Vedas knowledge and fundamental principles are presented for motivating readers to practice our valuable principles in modern lifestyle. Salient features:

- Content of the book aligned with the mapping course outcomes, programs and Units Outcomes.
- In the beginning of each unit learning outcomes are listed to make the student understand what is expected out of him/her after completing that unit.
- Book provides lots of recent information, interesting facts, QR code for E-resources, QR code for use of ICT, projects, group discussion etc.
- Student and teacher centric subject materials included in book with balanced and chronological manner.
- Figures, tables, and software screen shots are inserted to improve clarity of the topics.
- Apart from essential information a 'know more' section is also provided in each unit to extend the learning beyond syllabus.
- Short questions, objective questions and long answer exercises are given for practice of students after every chapter.
- Solved and unsolved problems including numerical examples are solved with systematic steps.



Table of Contents

Foreword Acknowledgement Preface Outcome Based Education Course Outcomes Guidelines for Teachers Guidelines for Students Abbreviations and Symbols List of Figures List of Tables

1. Structure of Computers
2. Micro Programmed Control
3. Microprocessor Architecture
4. Assembly Language Programming
5. Memory and Digital Interfacing

References for Further Learning CO and PO Attainment Table Index

Author

Dr. Sonal Yadav Assistant Professor Dept. of Computer Science & Engineering (CSE) National Institute of Technology Raipur, Chhattisgarh
